



OFFICE OF THE UNDER SECRETARY OF DEFENSE
3000 DEFENSE PENTAGON
WASHINGTON, DC 20301-3000

June 8, 2006

ACQUISITION
TECHNOLOGY
AND LOGISTICS

MEMORANDUM FOR DEFENSE MICROCIRCUITS PLANNING GROUP

SUBJECT: Joint Standardization Board for Microcircuits and Semiconductors

The attached charter establishing the Defense Microcircuits Planning Group as a Joint Standardization Board (JSB) for Microelectronics & Semiconductors under the Defense Standardization Program (DSP) is approved. The objective of the JSB is to achieve common, mutually satisfactory solutions to shared requirements and problems. The JSB will:

- Develop and improve processes and procedures to facilitate microelectronic and semiconductor component initiatives;
- Establish DOD standards (IAW DOD 4120.24-M, Defense Standardization Program (DSP) Policies and Procedures;
- Achieve interoperability, promote standardization, and conserve resources in support of Joint Service and Multinational Operations;
- Establish an effective, Department-wide forum for interoperability coordination, planning, and decision making

Your participation in the DSP's efforts to achieve interoperability and standardization for electronic components is appreciated.

My focal point for JSBs is Mr. Mike Goy, Defense Standardization Program Office, (703) 767-6882, Mike.Goy@dla.mil.

James D. Hall
Defense Standardization Executive

Attachment
As Stated



CHARTER
DoD JOINT STANDARDIZATION BOARD (JSB)
FOR
DEFENSE MICROELECTRONICS AND SEMICONDUCTORS.

Name of Group: DoD Joint Standardization Board for Defense Microelectronics and Semiconductors.

Purpose: The purpose of the DoD JSB for Defense Microelectronics and Semiconductors is to:

- Provide senior level (USDAT&L) visibility for standardization and interoperability initiatives.
- Provide a forum to discuss technical issues that have a major impact on defense microelectronic and semiconductor standardization.
- Provide a forum to discuss standardization documents and their impact on the standardization and interoperability as it applies to defense microelectronics and semiconductors.
- Provide the services a forum to outline their goals, objectives and critical issues.
- Provide a forum for the services, industry and OSD to discuss future program direction.
- Facilitate the discussion of current issues and the future need for DoD specifications and standards and other related or Non-Government Standards documents (DoD 4120.24-M, Defense Standardization Program, Policies & Procedures).
- Provide a forum to discuss issues on microelectronics and semiconductors as they affect interoperability.
- Promote the standardization of microelectronics and semiconductors since those parts have proven to have lowered costs, reduced inventories, shortened logistics chains and improved readiness.
- Facilitate funding for specific efforts/projects related to standardization and interoperability goals and objectives.
- Provide the interface for commercial/military integration.

Process: The DoD JSB for Defense Microelectronics and Semiconductors will meet on an ongoing basis to discuss those topics necessary to achieve standardization and interoperability objectives through the following:

- Discuss and provide direction on the impact of national and international standards as they relate to defense microelectronics and semiconductors.
- Promote commonality of microelectronics and semiconductors to enhance supportability and interoperability.
- Promote standardization of testing and evaluation of microelectronics and semiconductors.
- Interface with industry, academia and government agencies as needed to keep pace with progressing technology.

- Provide mechanism for exchange of technical information amongst military departments, industry, and other interested organizations on microelectronics and semiconductors as related to interoperability and commonality of equipment and operational requirements.
- Discuss and provide direction on microelectronics and semiconductors issues that affect standardization and interoperability with industry and industry associations.
- Document and maintain adequate and uniform meeting minutes including background and supporting documentation.

Authority: Defense Standardization Executive (ADUSD/LP&P) Memo, Dated: 8 June 2006, Subject: Joint Standardization Boards.

Membership: The Board shall be composed of technical experts in microelectronics and semiconductors and shall include, as a minimum, members from Defense Standardization Program Office (DSPO), the services (Army, Navy, Air Force and NASA), Defense Supply Center, Columbus (DSCC), and other government agencies. The JSB will maintain liaisons with other interested parties in industry, industry associations (e.g., Electronic Industries Alliance, G-12 and JEDEC committees), and academia. The Chairperson will be the principal DSPO Government member.

Meetings and Procedures: The JSB will meet a minimum of once each fiscal year or upon request by a voting board member in an open general session to perform the group's mission. Meetings will be scheduled by the current Chairperson to accommodate attendance by members. Representation from the board member organizations is required for all meetings. Functional areas and interested organizations will be invited to open general sessions. List of prioritized items will be made a matter of record in the meeting minutes of the general session and assigned a focal point to complete tasks as designated by the Chairperson. The Chairperson has the authority to schedule additional meetings or set up subcommittees and working groups in coordination with the principal members, as required, to perform the group's mission.

Duration: Serves as an ongoing group as determined by JSB membership.